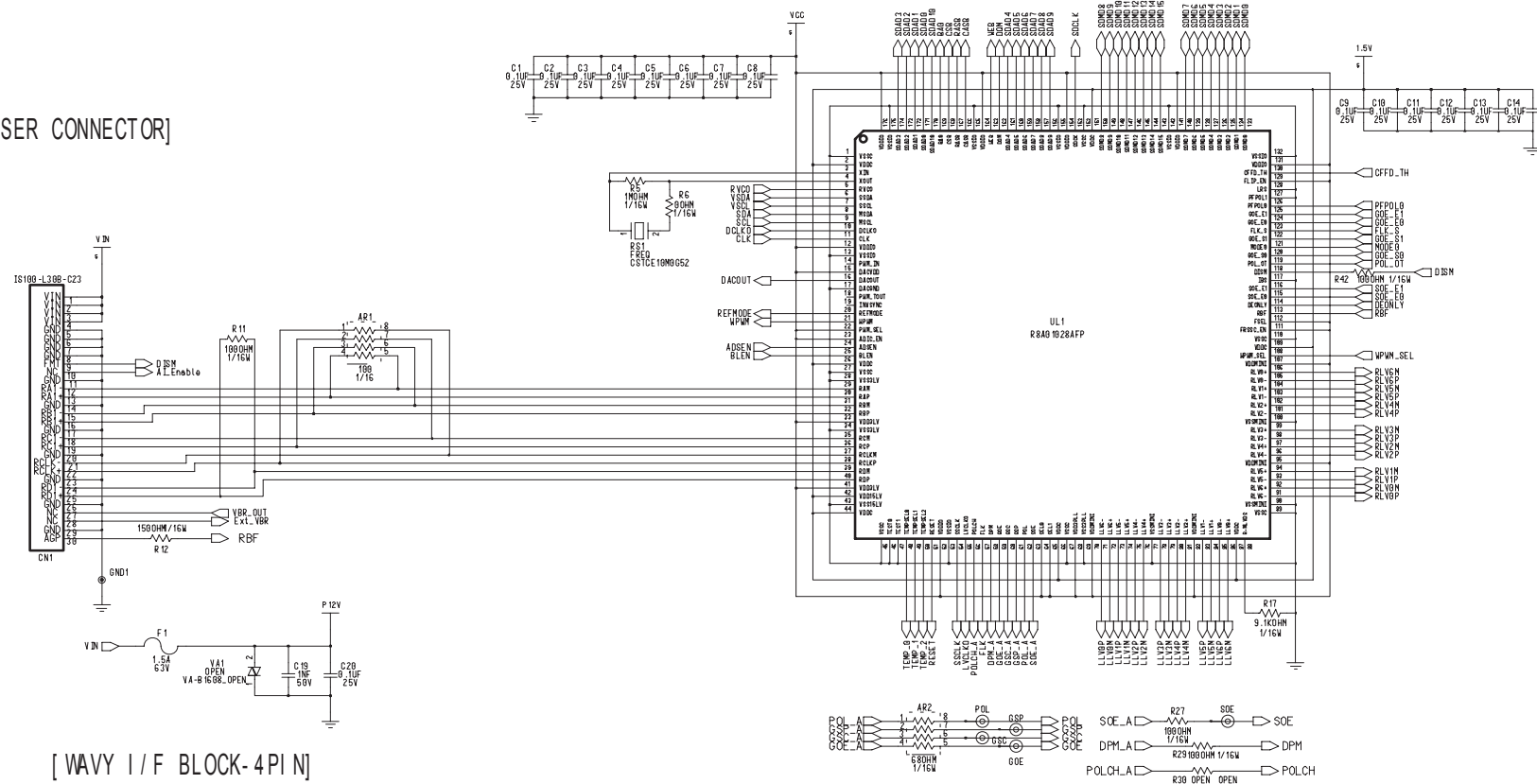
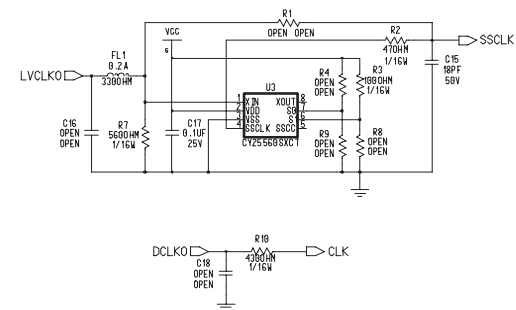


[T_CON BLOCK]

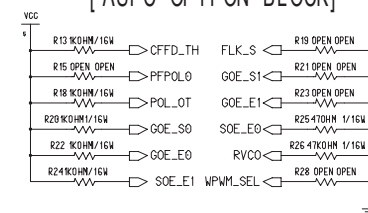
USER CONNECTIONS



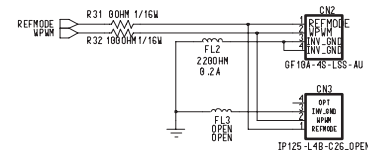
SPREAD SPECTRUM



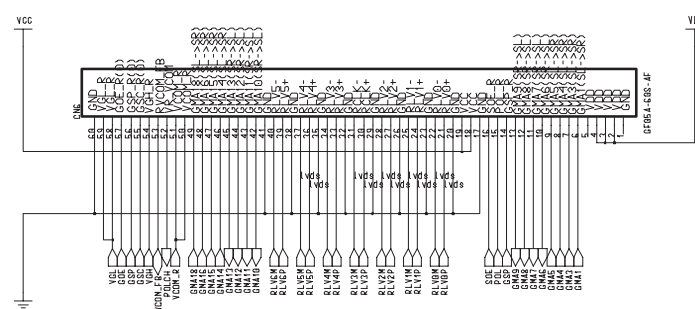
[ASIC OPTION BLOCK]



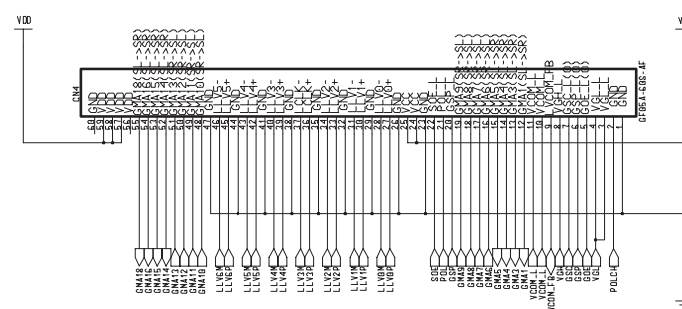
[WAVY I / F BLOCK-4PI N



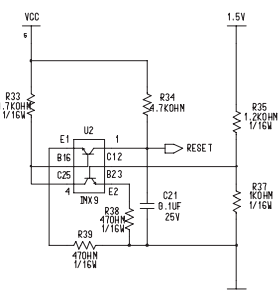
CONTROL-SOURCE RIGHT CONNECTOR



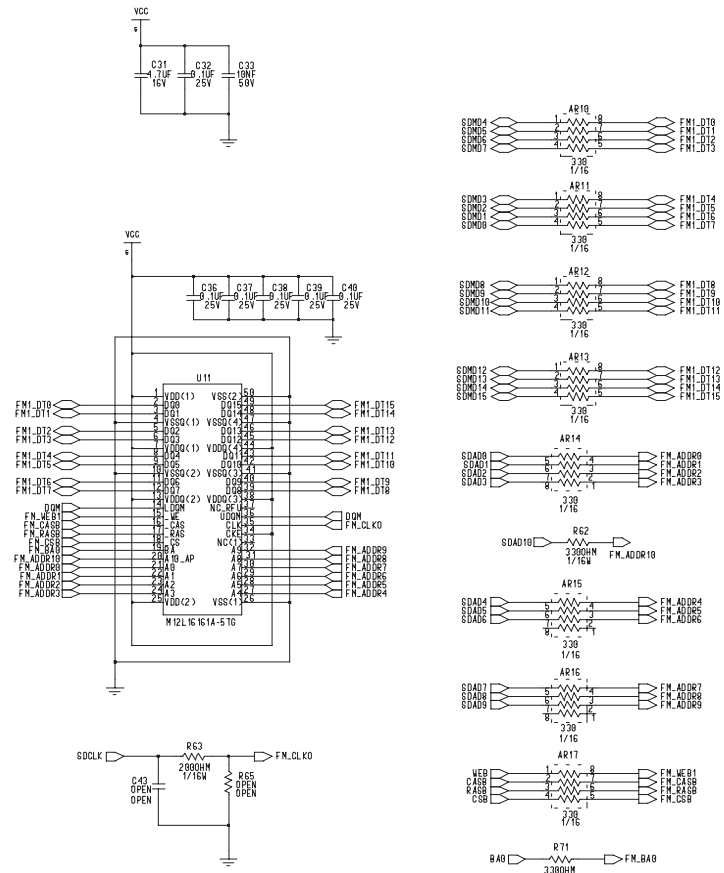
[CONTROL - SOURCE LEFT CONNECTOR



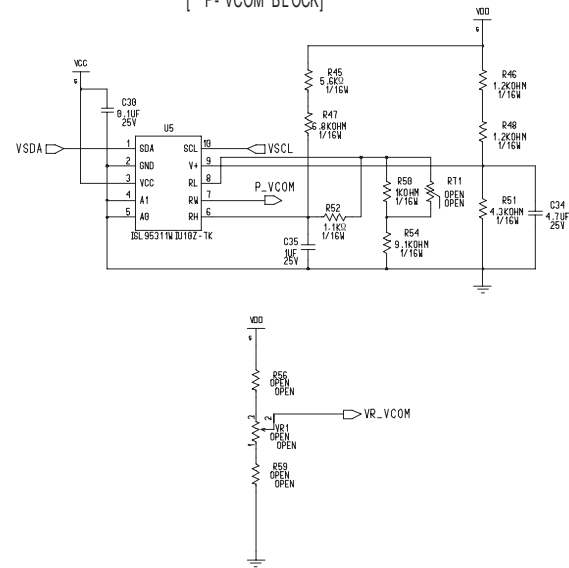
[RESET BLOCK



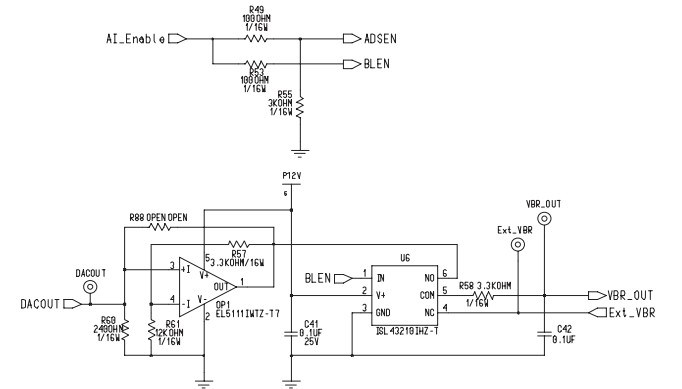
[SDRAM I / F B L O C K]



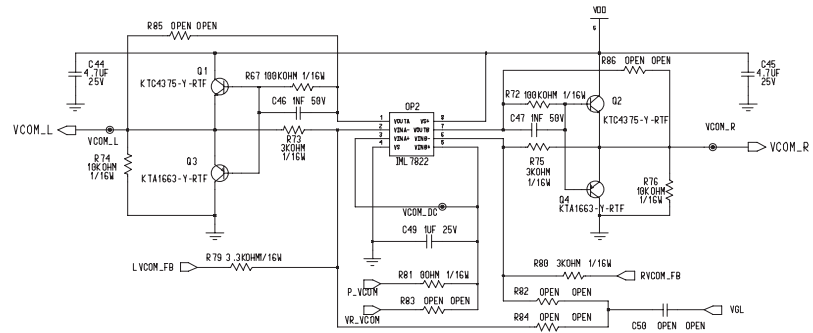
[P- VCOM BLOCK]



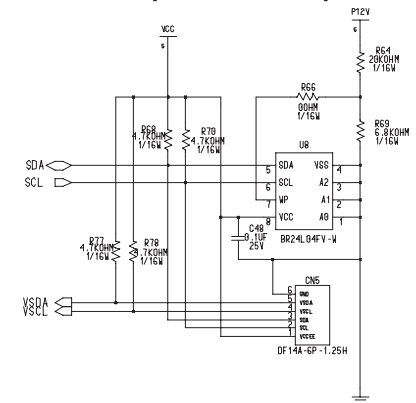
[A I I / F B L O C K]

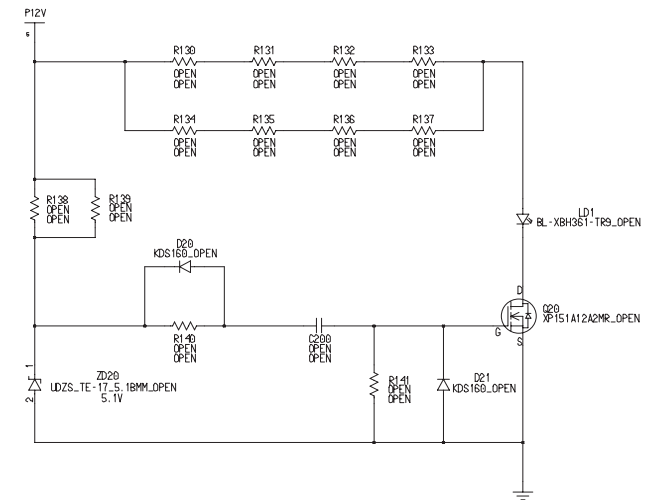
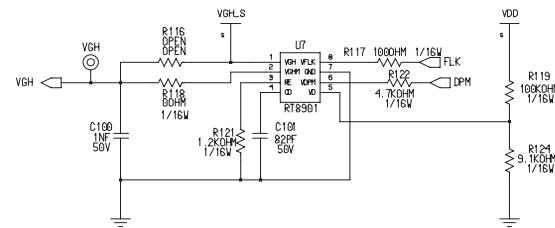
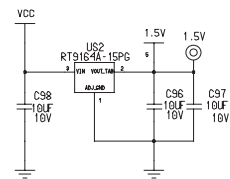
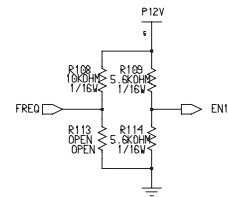


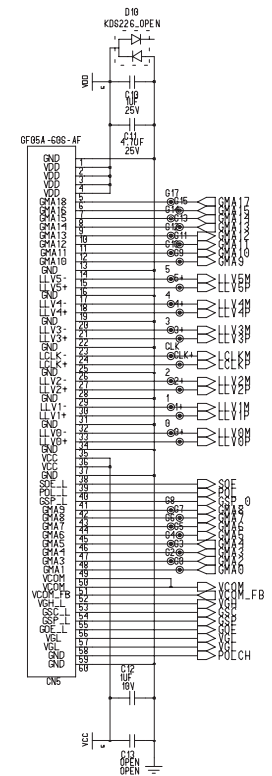
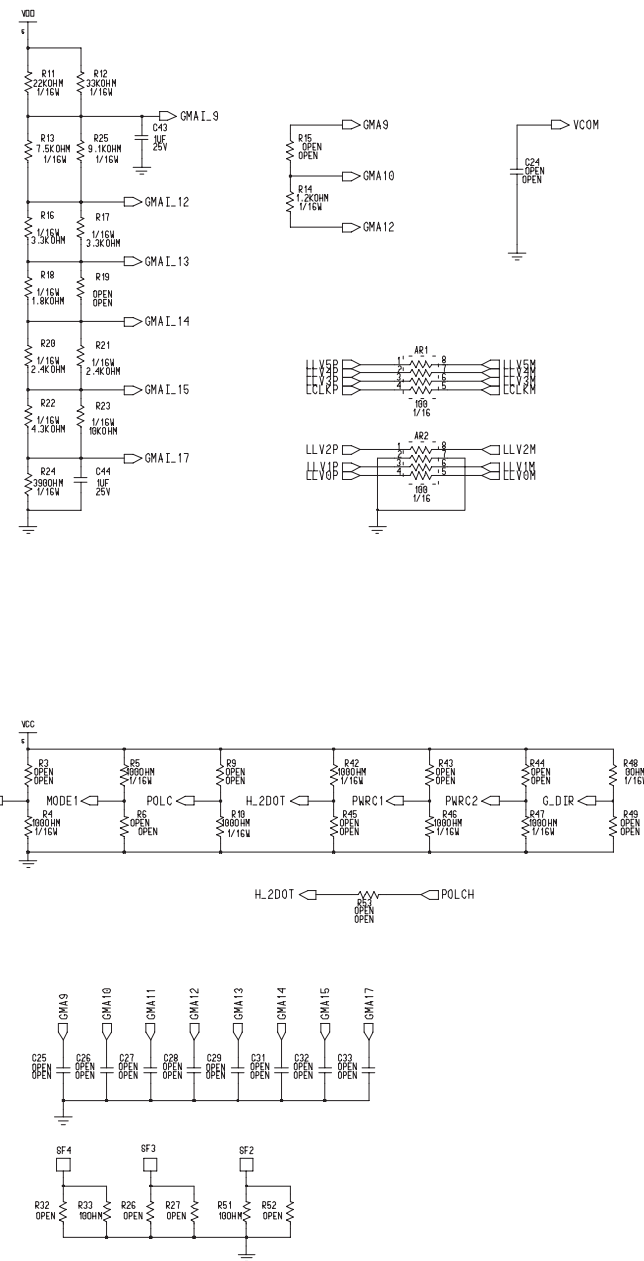
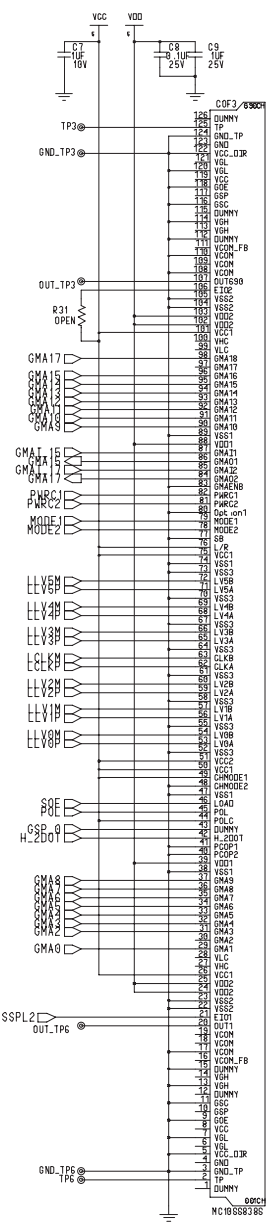
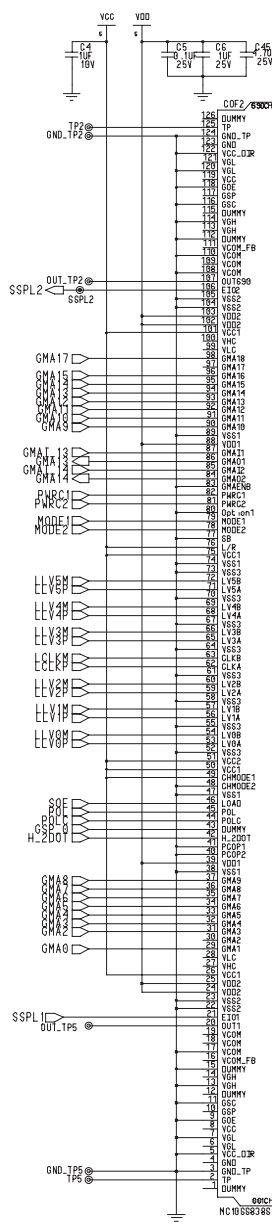
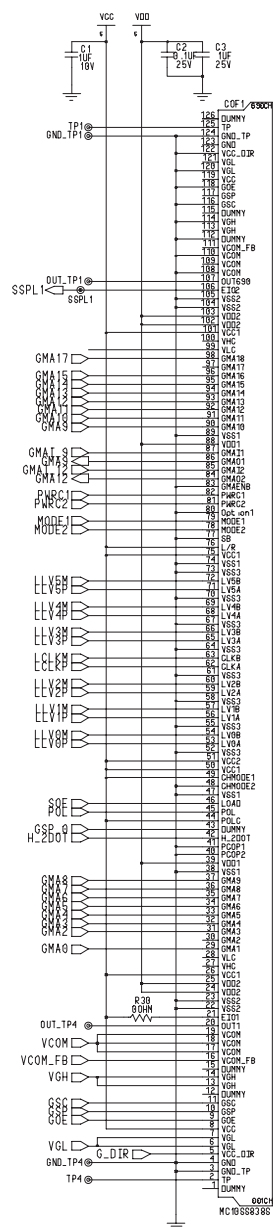
[VCOM COMPENSATION BLOCK]



[EEPROM BLOCK]





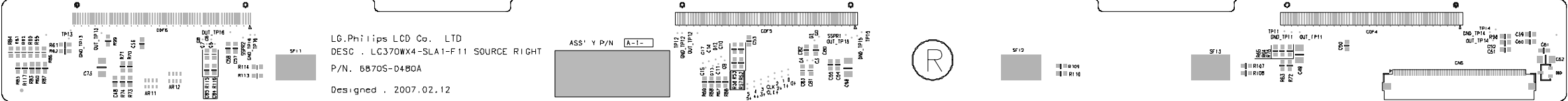


[H_2DOT] : Inversion Select Pin
H : Horizontal 2-Dot Inversion Enable
L : Horizontal 2-Dot Inversion Disable

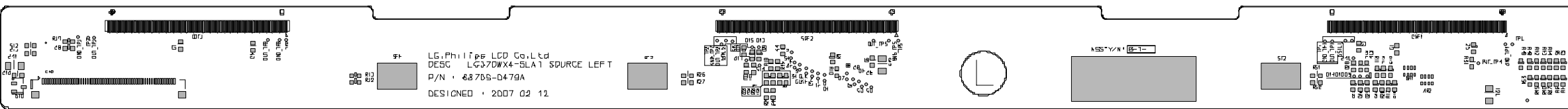
[PWRC1/PWRC2] : Output Power Control Pin
H/H : High Power Mode
H/L : Normal Mode
L/H : Low Power Mode
L/L : UltraLow Power Mode

Printed Circuit Board / 회로기판

SOURCE(Right_Top)



SOURCE(Left_Top)



Control(Top)

